UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,912,882 B2 Page 1 of 1

APPLICATION NO. : 10/561558 DATED : March 22, 2011

INVENTOR(S) : Kim

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4, line 45, in Claim 1, delete "into DDS" and insert -- into a DDS --.

Column 4, line 57, in Claim 1, delete "click" and insert -- clock --.

Column 4, line 58, in Claim 1, delete "multipler" and insert -- multiplier --.

Column 4, line 60, in Claim 1, delete "frequency," and insert -- frequency; --.

Column 4, line 65, in Claim 1, delete "analog the" and insert -- analog --.

Column 5, line 2, in Claim 2, delete "10× PLL" and insert -- 10× PLL --.

Column 5, line 4, in Claim 3, delete "below" and insert -- below, --.

Column 5, line 18, in Claim 6, delete "bandpass" and insert -- band pass --.

Column 6, line 8, in Claim 12, delete "phase a" and insert -- phase --.

Signed and Sealed this Sixteenth Day of August, 2011

David J. Kappos

Director of the United States Patent and Trademark Office